CLAIMS:

What is claimed is:

1. A method in a data processing system for transferring data from a memory to a network adapter, the method comprising:

receiving a request to transfer data to a network adapter; and

setting an offset for a starting address of the data to align the data with an end of a frame in the memory, wherein the frame is transferred from the memory to the network adapter.

2. The method of claim 1 further comprising:

initiating a transfer of the frame from the host/system memory to the network adapter.

3. The method of claim 1, wherein the cache line size is 2^n, wherein n is a positive integer.

4. The method of claim 1, wherein the data is transferred from the memory to the network adapter through a bridge chip.

5. The method of claim 1, wherein the offset is zero if a frame size of the frame divided by a cache line size is zero.

6. A method in a data processing system for transferring data from a memory to a network adapter, the method comprising:

identifying an amount of the data;

if the frame size for a frame is divisible by a cache line size with a remainder, setting an offset for the data to align the data to an end of the frame; and

if the frame size for if the frame size divided by the cache line size without a remainder, setting the offset to zero.

7. The method of claim 6, wherein the offset is determined using the following:

offset = CLS - (frame size - ABS(frame size/CLS)*CLS

wherein CLS is the cache line size.

8. The method of claim 6 further comprising:

offsetting the data in the frame using the offset.

9. The method of claim 7 further comprising:

transferring the frame to the network adapter after offsetting the data using the offset.

10. A means in a data processing system for transferring data from a memory to a network adapter, the data processing system comprising:

receiving means for receiving a request to transfer data to a network adapter; and

setting means for setting an offset for a starting address of the data to align the data with an end of a frame in the memory, wherein the frame is transferred from the memory to the network adapter.

11. The data processing system of claim 10 further comprising:

initiating means for initiating a transfer of the frame from the memory to the network adapter.

12. The data processing system of claim 10, wherein the cache line size is 2^n, wherein n is a positive integer.

13. The data processing system of claim 10, wherein the data is transferred from the memory to the network adapter through a bridge chip.

14. The data processing system of claim 10, wherein the offset is zero if a frame size of the frame divided by a cache line size is zero.

15. A means in a data processing system for transferring data from a memory to a network adapter, the data processing system comprising:

identifying means for identifying an amount of the data;

first setting means for setting an offset for the data to align the data to an end of the frame if the frame size for a frame is divisible by a cache line size with a remainder; and

second setting means for setting the offset to zero if the frame size for if the frame size divided by the cache line size without a remainder.

16. The data processing system of claim 15, wherein the offset is determined using the following:

offset = CLS - (frame size - ABS(frame size/CLS)*CLS

wherein CLS is the cache line size.

17. The data processing system of claim 15 further comprising:

offsetting means for offsetting the data in the frame using the offset.

18. The data processing system of claim 16 further comprising:

transferring means for transferring the frame to the network adapter after offsetting the data using the offset.

19. A computer program product in a computer readable medium for transferring data from a memory to a network adapter, the computer program product comprising:

first instructions for receiving a request to transfer data to a network adapter; and

second instructions for setting an offset for a starting address of the data to align the data with an end of a frame in the memory, wherein the frame is transferred from the memory to the network adapter.

20. A computer program product in a computer readable medium for transferring data from a memory to a network adapter, the computer program product comprising:

first instructions for identifying an amount of the data;

second instructions for setting an offset for the data to align the data to an end of the frame if the frame size for a frame is divisible by a cache line size with a remainder; and

third instructions for setting the offset to zero if the frame size for if the frame size divided by the cache line size without a remainder.

21. A server data processing system for obtaining cultural context information from a client, the server data processing system comprising:

a bus system;

a network adapter connected to the bus system;

a memory connected to the bus system, wherein the memory includes a set of instructions; and

a processing unit connected to the bus system, wherein the processing unit executes a set of instructions to receive a request to transfer data to a network adapter; and set an offset for a starting address of the data to align the data with an end of a frame in the memory, wherein the frame is transferred from the memory to the network adapter.

22. A server data processing system for obtaining cultural context information from a client, the server data processing system comprising:

a bus system;

a network adapter connected to the bus system;

a memory connected to the bus system, wherein the memory includes a set of instructions; and

a processing unit connected to the bus system, wherein the processing unit executes a set of instructions to identify an amount of the data; set an offset for the data to align the data to the end of the frame if the frame size for a frame is divisible by a cache line size with a remainder; and to set the offset to zero if the frame size is divided by the cache line size without a remainder.